5019860
仮図面

FIG.1

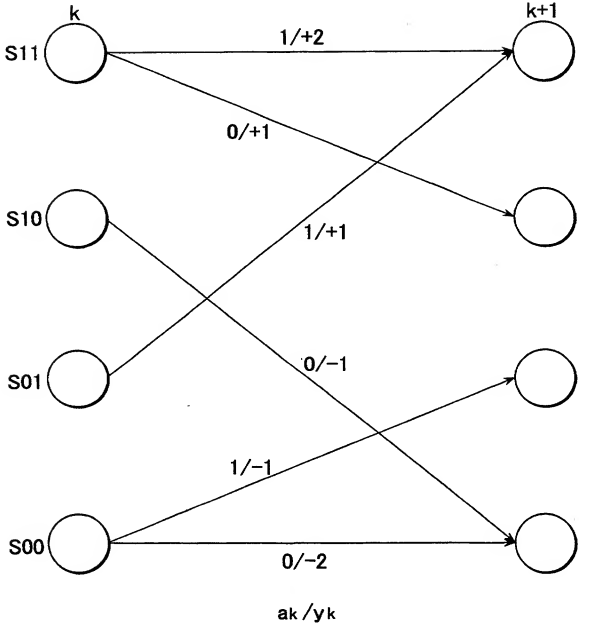


FIG.2

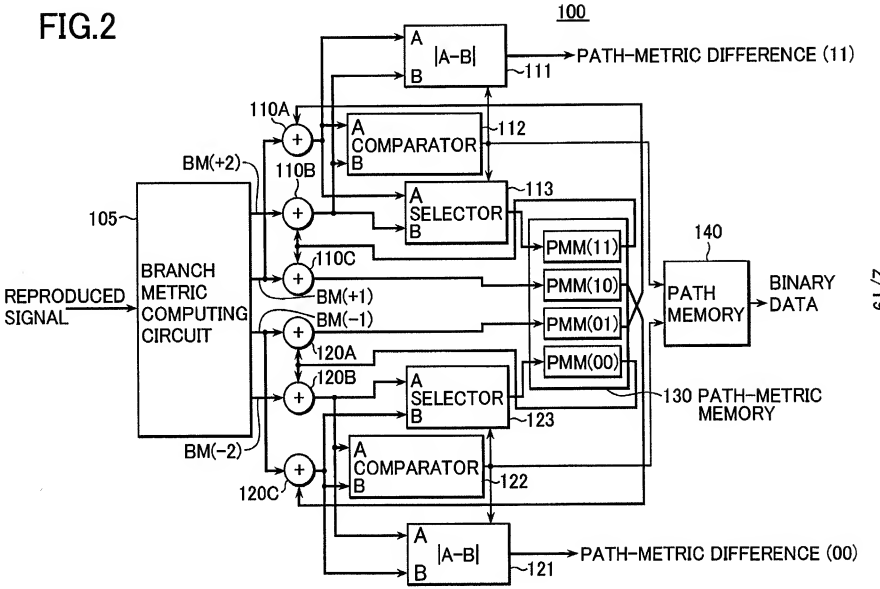
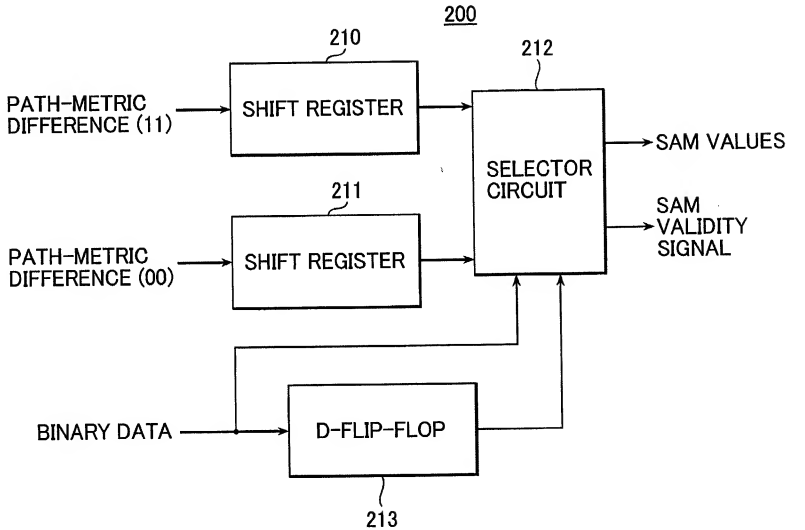

100
A
|A-B|
B
111
PATH-METRIC DIFFERENCE (11)
110A
A
COMPARATOR
B
112
BM(+2)
110B
A
SELECTOR
B
113
105
140
PMM(11)
PMM(10)
PMM(01)
PMM(00)
110C
BRANCH
METRIC
COMPUTING
CIRCUIT
PATH
MEMORY
BINARY
DATA
REPRODUCED
SIGNAL
BM(+1)
BM(−1)
120A
120B
A
SELECTOR
B
123
130 PATH-METRIC
MEMORY
BM(−2)
A
COMPARATOR
B
122
120C
A
|A-B|
B
121
PATH-METRIC DIFFERENCE (00)

FIG.3

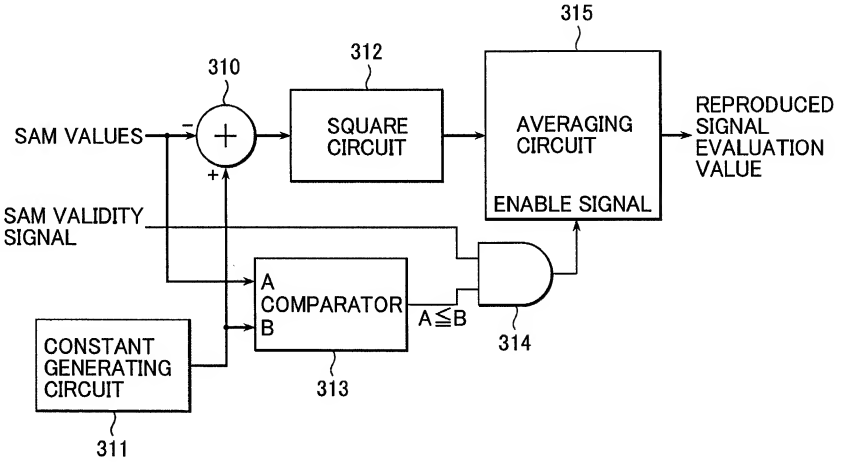
BINARY DATA ARRAY	SAM
00	PATH-METRIC DIFFERENCE(00)
01	VOID
10	VOID
11	PATH-METRIC DIFFERENCE(11)

FIG.4


200
210
212
PATH-METRIC DIFFERENCE (11)
SHIFT REGISTER
SELECTOR CIRCUIT
SAM VALUES
SAM VALIDITY SIGNAL
211
PATH-METRIC DIFFERENCE (00)
SHIFT REGISTER
BINARY DATA
D-FLIP-FLOP
213

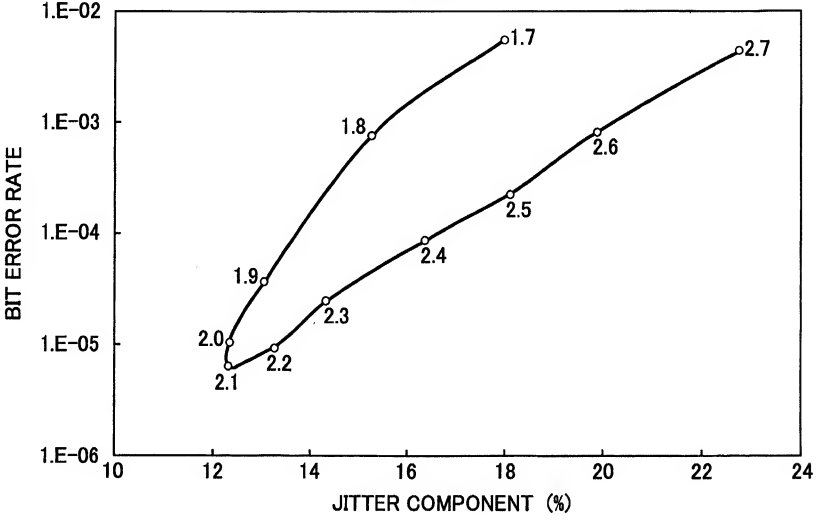
FOMTEF 9S1Z4OOF

FIG.5



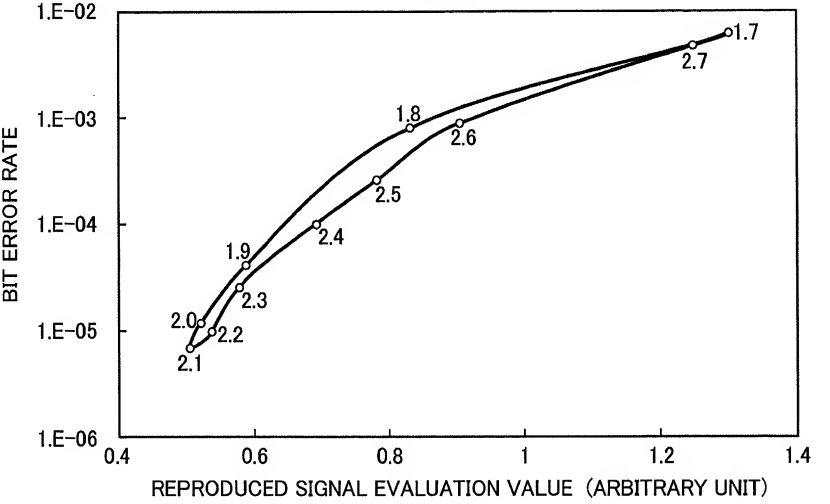
FORMED SHEET

FIG.6



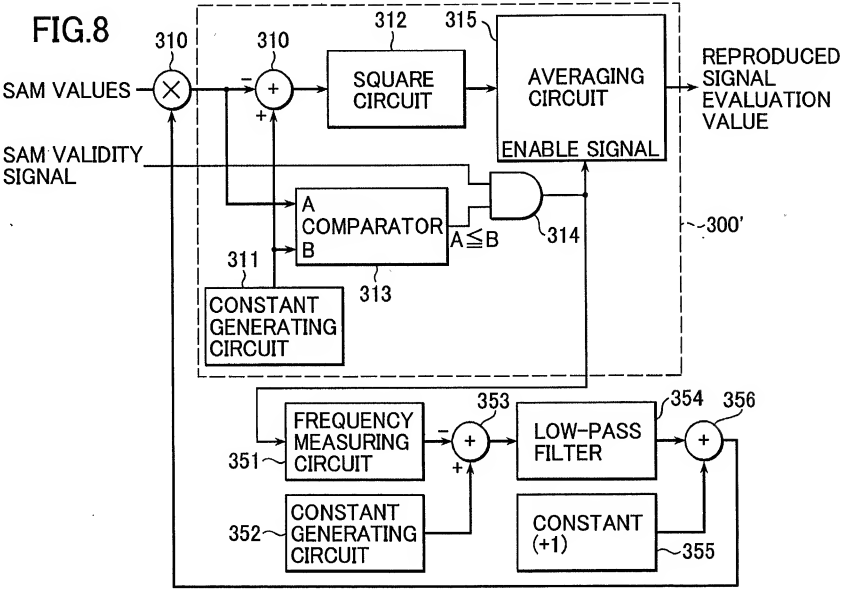
FOMTEF 3SEA7OOT

FIG.7



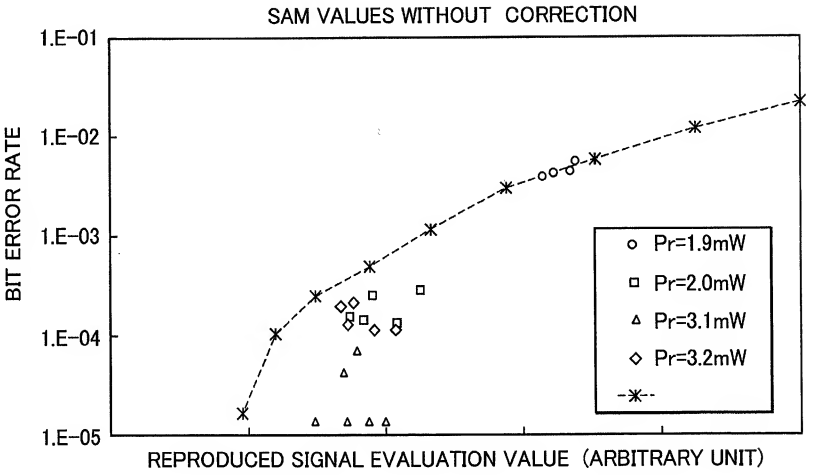
EDITED 95/2/10

FIG.8



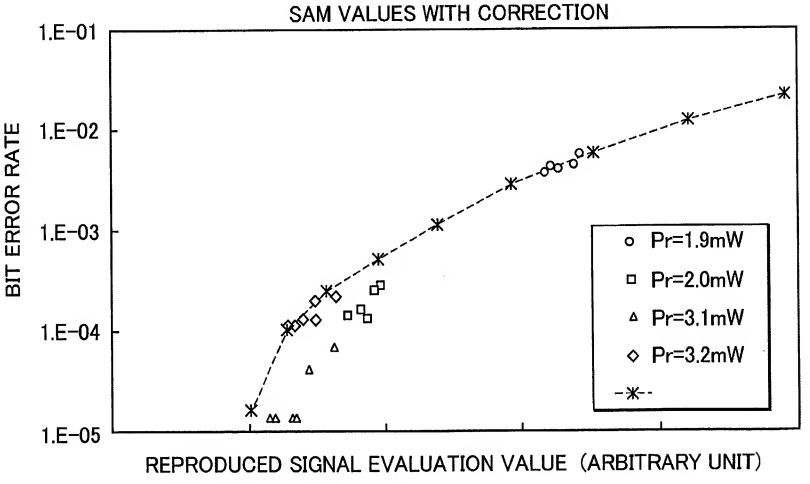
FOREST SELECT

FIG.9



FOMEF 95E7F001

FIG.10



JAPIO - 2004-07-08

FIG.11

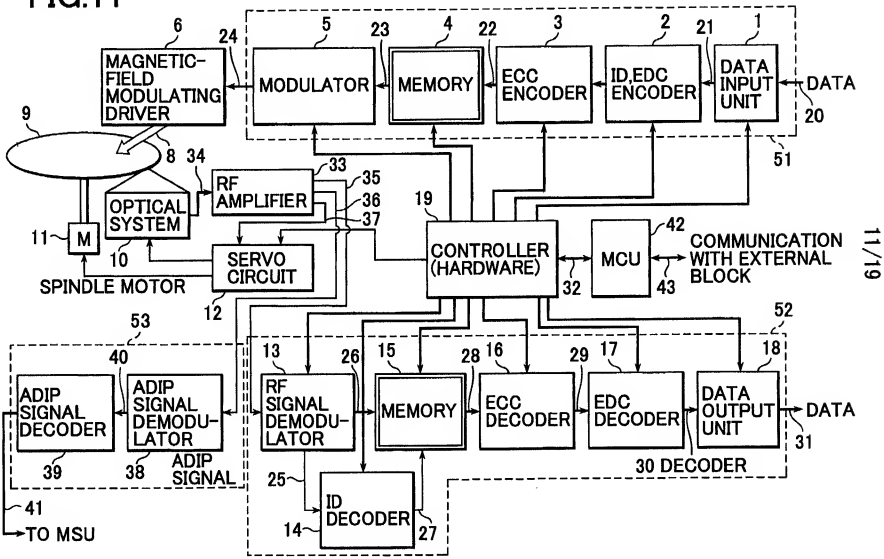
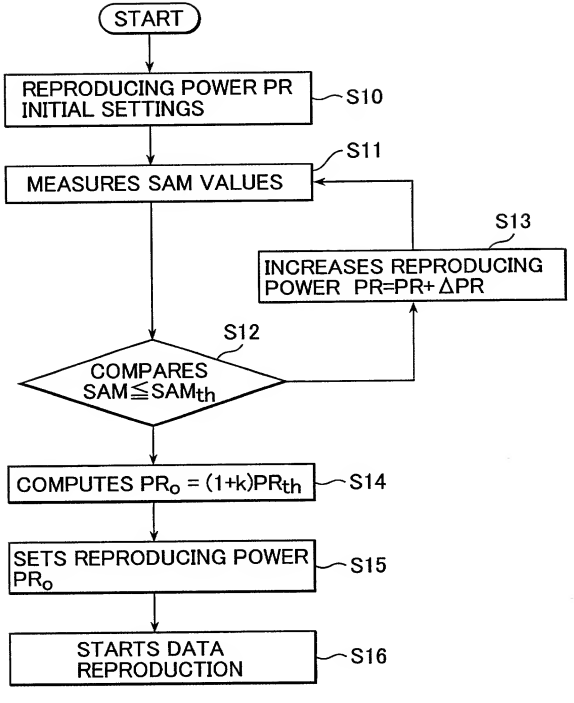


FIG.12


START
REPRODUCING POWER PR INITIAL SETTINGS
S10
S11
MEASURES SAM VALUES
S13
INCREASES REPRODUCING POWER PR=PR+ΔPR
S12
COMPARES SAM≦SAMth
COMPUTES PRo = (1+k)PRth
S14
SETS REPRODUCING POWER PRo
S15
STARTS DATA REPRODUCTION
S16

FOMEP 95E4F08F

FIG.13

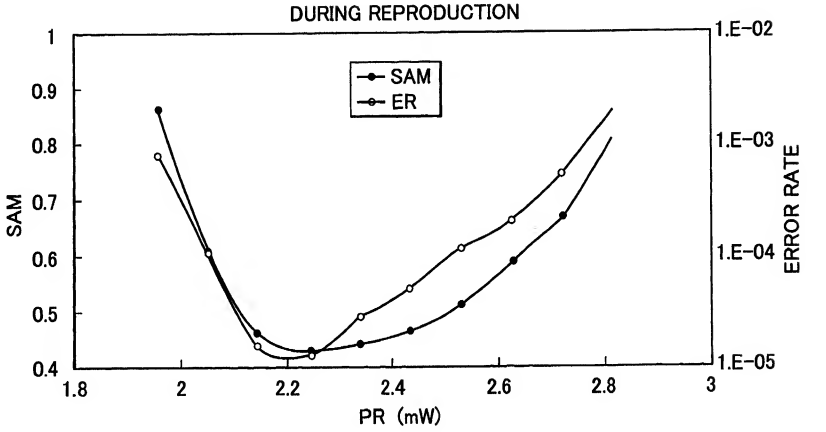
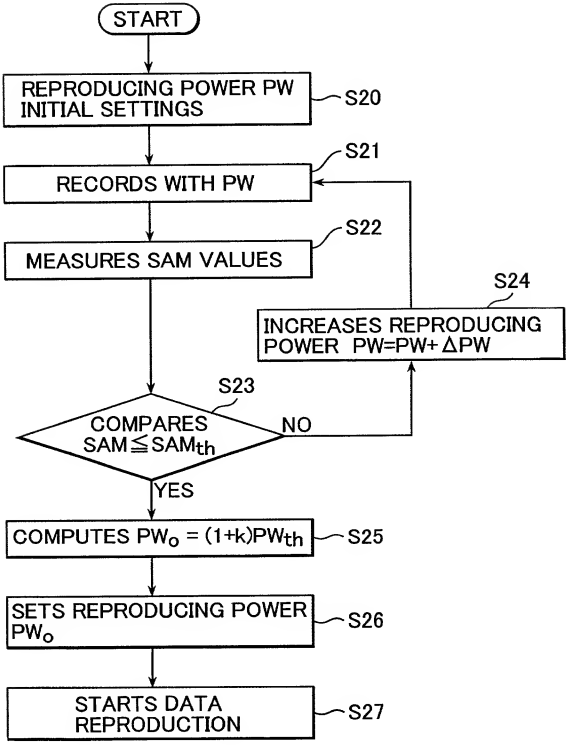
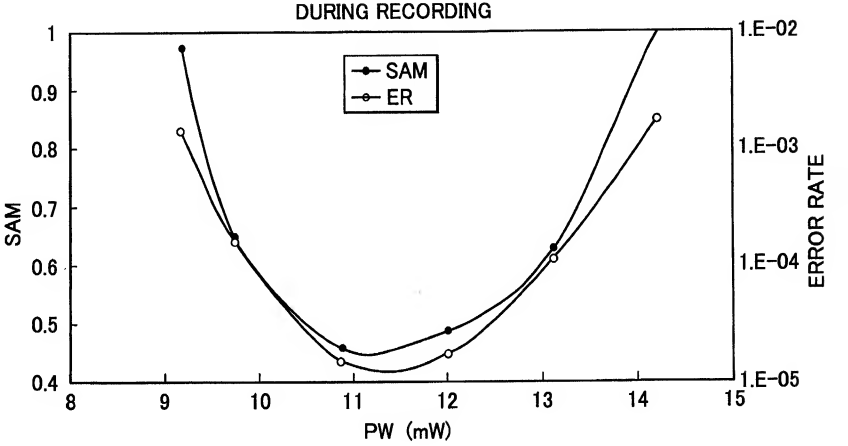


FIG.14


START
REPRODUCING POWER PW INITIAL SETTINGS
S20
RECORDS WITH PW
S21
MEASURES SAM VALUES
S22
COMPARES SAM≦SAMth
S23
NO
YES
INCREASES REPRODUCING POWER PW=PW+ΔPW
S24
COMPUTES PWo = (1+k)PWth
S25
SETS REPRODUCING POWER PWo
S26
STARTS DATA REPRODUCTION
S27

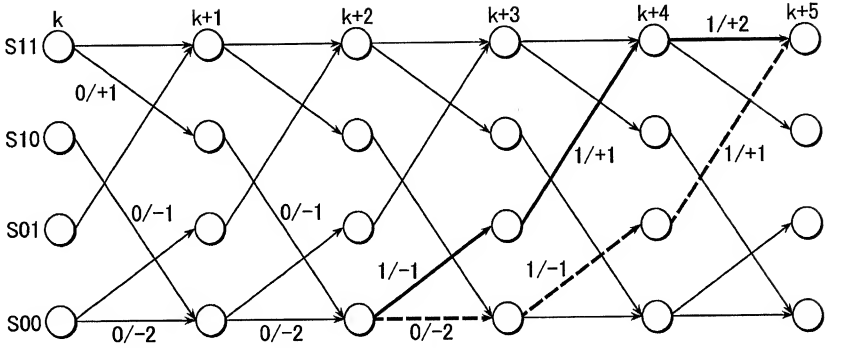
FOREST-3512100T

FIG.15



EUROPEAN SEARCH REPORT

FIG.16



PCT/JP2004/000000

FIG.17

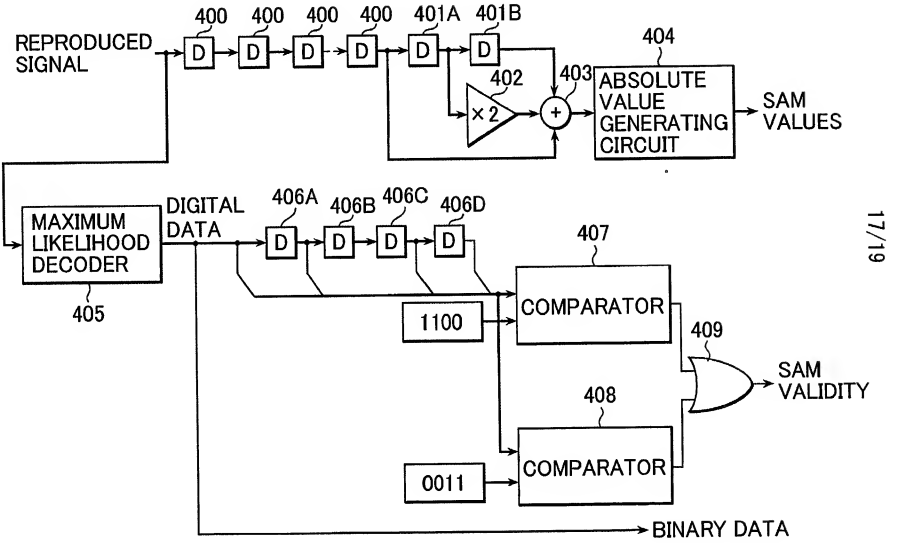
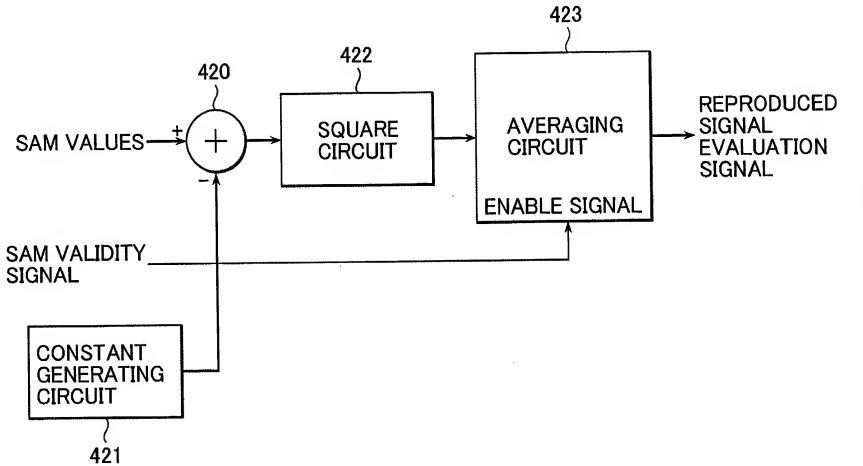


FIG.18


423
422
420
SAM VALUES
+
−
SQUARE CIRCUIT
AVERAGING CIRCUIT
ENABLE SIGNAL
REPRODUCED SIGNAL EVALUATION SIGNAL
SAM VALIDITY SIGNAL
CONSTANT GENERATING CIRCUIT
421

JAPIO: 3514/2005

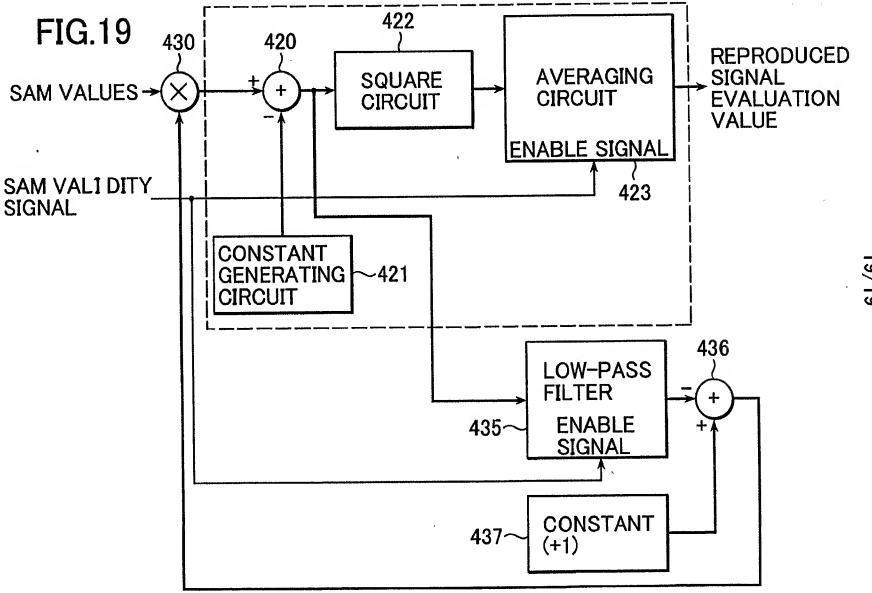



FIG.19